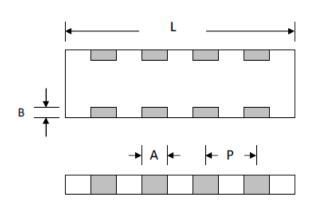
CHIP CAPACITOR ARRAY

CATYPE

FEATURES

- Two standard sizes.
- Less than 50% board space of an equivalent discrete component.
- High volumetric efficiency.
- Dense dielectric layers.
- Supplied in tape on reel or loose in bag.
- Increased throughput, by time saved in mounting.
- Cost savings on manufacturing time.
- Reel quantity is 4Kpcs per reel for CA0603 and 10Kpcs for CA0402.



Unit : mm

DIMENSIONS

TYPE	L	W	MIN.	T MAX.	Α	В	Р
CA0402 4X0402)	2.0±0.15	1.25±0.15	0.6	0.95	0.28±0.1	0.20±0.10	0.5±0.10
CA0603 (4X0603)	3.2±0.15	1.60±0.15	0.80	1.20	0.40±0.1	0.30±0.15	0.8±0.15

\rightarrow	Т	•	_
F			†
			W
			<u></u>

TYPE	NPO (±5%, ±10%)	X7R (±10%, ±20%)	Y5V (-20%~+80%)
CA0402	15PF~270PF	1NF (50V) 10NF~47NF (16V)	_
CA0603	10PF∼1NF	220PF~100NF	10NF~100NF

CHARACTERISTICS

IEC384-10	TEST	CONDITIONS	REQUIREMENTS			
			NPO	X7R	Y5V	
4.9	Bending	Bending rate 1mm/s, jig, Radius 340mm	Δ C/C≦1%	ΔC/C≦10%	ΔC/C≦20%	
4.10	Resistance to soldering heat	260±5°C for 10±0.5s in static solder bath	Δ C/C≦0.5% or 0.5pF, whichever is greater	-5%≦ Δ C/C 10%	-10% ≦ Δ C/C 20%	
4.11	Solderability	235±5°C for 2±0.5s in a static solder bath				
4.12	Rapid change in temperature	NPO/X7R: -55°C to+125°C, 5 cycles Y5V: -25°C to+85°C, 5 cycles	ΔC/C≦1% or 1pF, whichever is greater	ΔC/C: ≦ 15%	ΔC/C:≦ 20%	
4.14	Damp heat, Steady state	At 40°C, 90 to 95% RH and Ur Applied for 56 days	Δ C/C; 2% or 1pF whichever is greater Tan δ ≦2 x specified Value IR: 2500M Ω or RxC≧25s whichever is less	Δ C/C; ≤15% Tan δ ≤ 7% IR: 1000MΩ or RxC≥25s whichever is less	Δ C/C; ≤30% Tan δ ≤ 7% IR: 1000MΩ or RxC≥25s whichever is less	
4.15	Endurance	At upper category temperature 2xUr applied for 42 days	Δ C/C; 2% or 1pF whichever is greater Tan δ ≦2 x specified Value IR: 4000M Ω or RxC≧ 40s whichever is less	Δ C/C; ≤15% Tan δ ≤ 7% IR: 2000MΩ or RxC≥50s whichever is less	Δ C/C; ≤30% Tan δ ≤ 7% IR: 2000MΩ or RxC≥50s whichever is less	